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CIRCUIT AND METHOD FOR IMPLEMENTING A LOW SUPPLY  
VOLTAGE CURRENT REFERENCE, Atty. Dkt. No. 2059/US/2, filed  
March 8, 2004, Cust. No. 20686, PH 303-629-3400

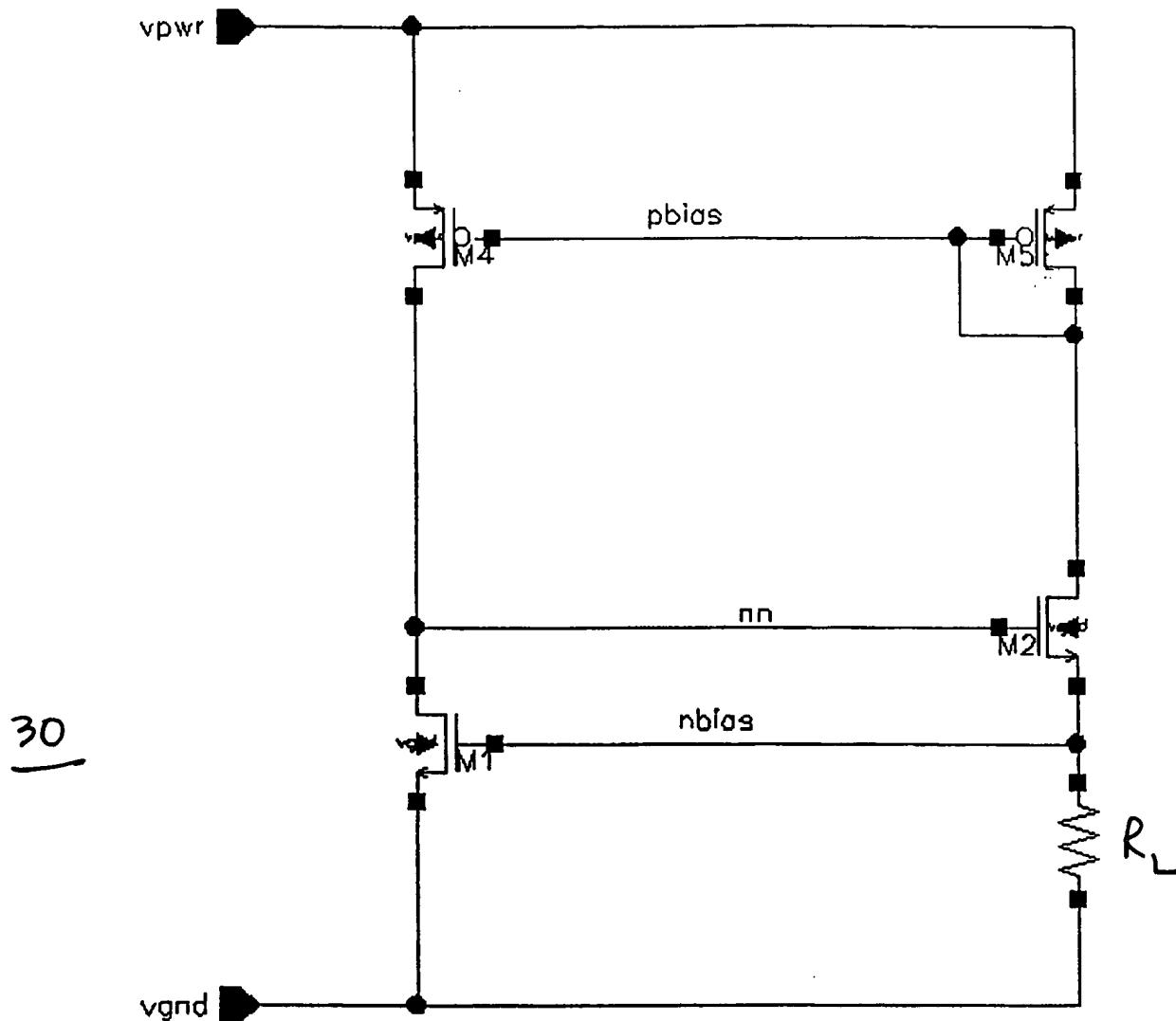


FIG. 1

CIRCUIT AND METHOD FOR IMPLEMENTING A LOW SUPPLY  
 VOLTAGE CURRENT REFERENCE, Atty. Dkt. No. 2059/US/2, filed  
 March 8, 2004, Cust. No. 20686, PH 303-629-3400

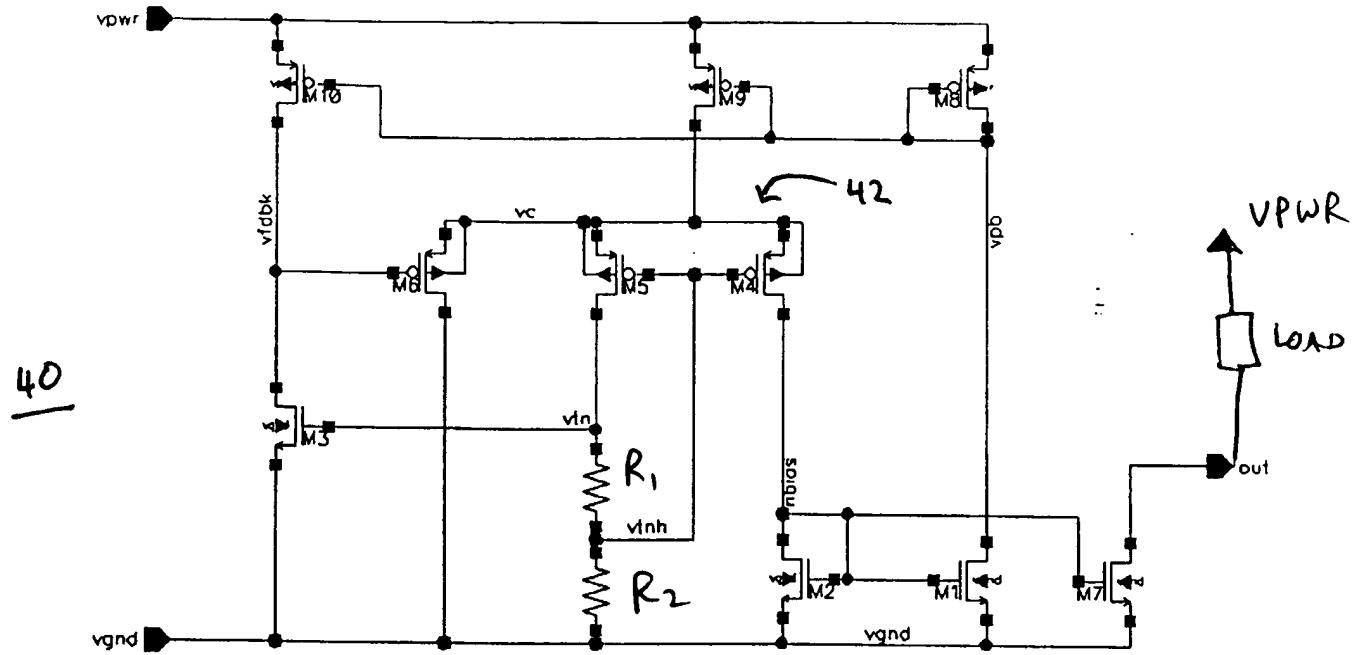


FIG. 2

CIRCUIT AND METHOD FOR IMPLEMENTING A LOW SUPPLY  
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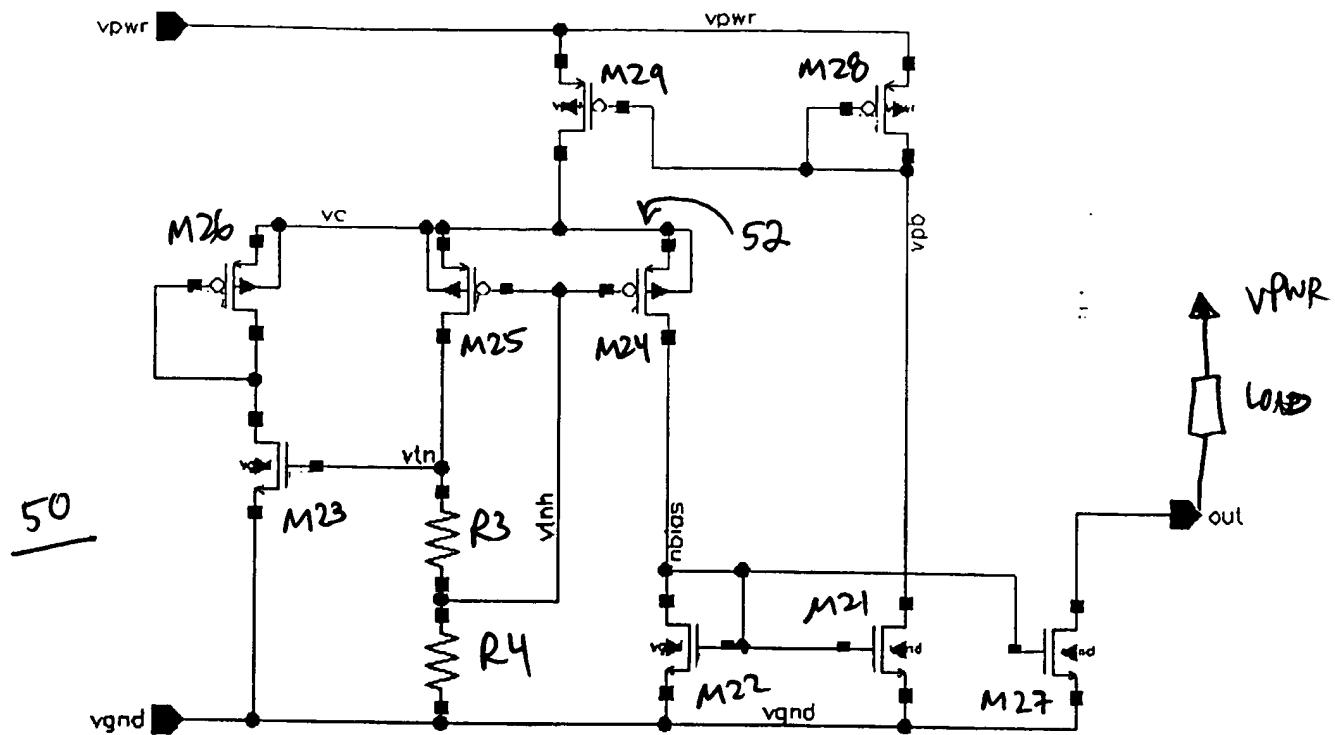


FIG. 3

CIRCUIT AND METHOD FOR IMPLEMENTING A LOW SUPPLY  
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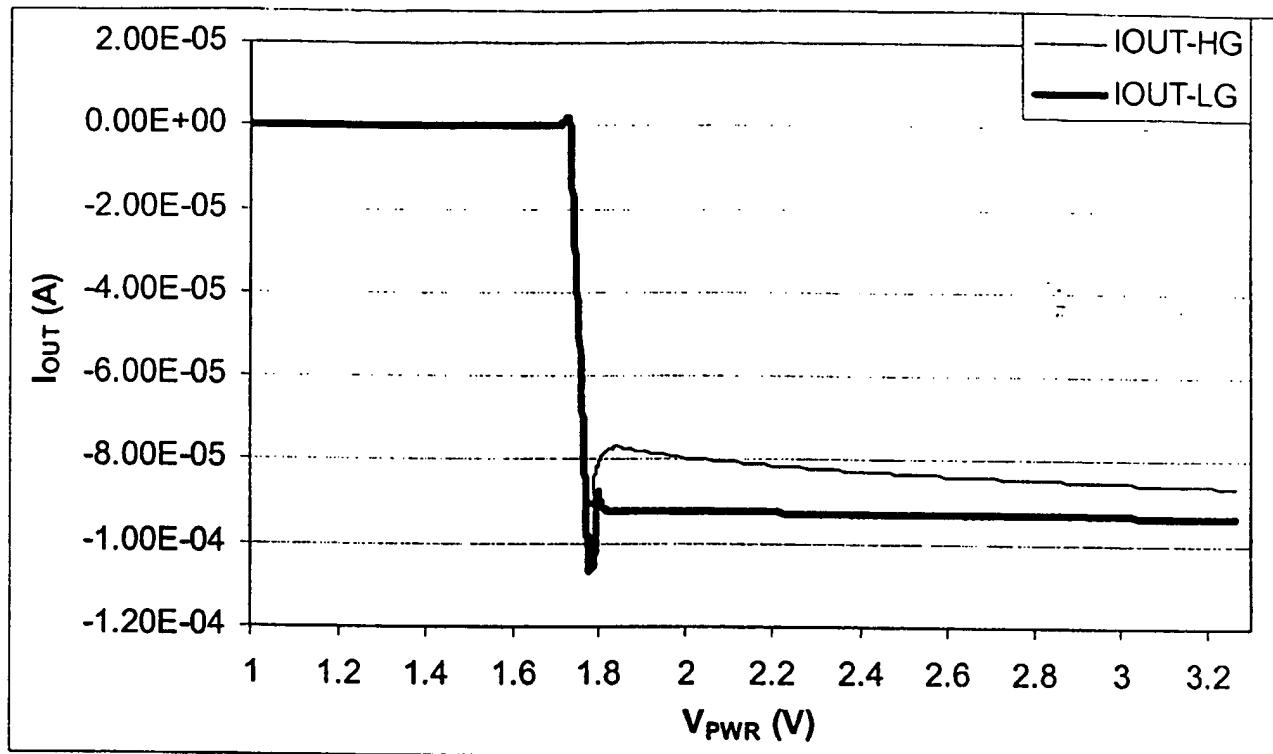


Fig. 4